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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR: Harry J. Beatty, III et al.)
SERIAL NO.:)
FILING DATE:)
FOR: PARALLEL SOFTWARE)
PROCESSING SYSTEM)

EXAMINER:

ART UNIT:

DATE:

jc531 U.S. PTO

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
BOX PATENT APPLICATION
Washington, D.C. 20231

Dear Sir:

In accordance with 37 CFR 1.56, 1.97 and 1.98, the following items are made of record to assist the Patent & Trademark Office in its examination of this application and is, in the opinion of the attorney designated below for applicant(s), information relevant to the closest prior art of which that person is aware. The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or that no other art than that identified exists.

PATENT NO.

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INVENTOR

Smith
Nikhil et al.
Noack
Gosling et al.
Sundaresan

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July 7, 1992
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EP 0 897 150 A2	EPC	17 February 1999
EP 0 899 660 A1	EPC	March 3, 1999
EP 0 909 094 A1	EPC	April 14, 1999
EP 0 936 544 A2	EPC	August 18, 1999
WO 99/10811	PCT	March 4, 1999

OTHER PUBLICATIONS

IBM Technical Disclosure Bulletin, Vol. 36, No. 12, December 1993, *Implementing A CSR-PCS as Multiple Threads*, pages 57-61.

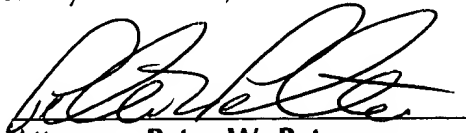
IBM Technical Disclosure Bulletin, Vol. 37, No. 01, January 1994, *Multisequencing In A Single Instruction Stream Processing Of Parallel Streams*, pages 133-139.

Krishna Kavi, Hyong-Shik-Kim, Joseph Arul of University of Alabama; Ali R. Hurson of Pennsylvania; *A Decoupled Scheduled Dataflow Multithreaded Architecture*; IEEE Proceedings, 8/99, pages 138-143.

A copy of each of the foregoing items and Form PTO 1449 are enclosed herewith.

Respectfully submitted,

By:



Attorney: **Peter W. Peterson**

Reg. No.: **31,867**

DeLIO & PETERSON, LLC
121 Whitney Avenue
New Haven, CT 06510
(203) 787-0595


CERTIFICATE OF MAILING UNDER 37 CFR 1.10

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, as "Express Mail Post Office to Addressee"

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